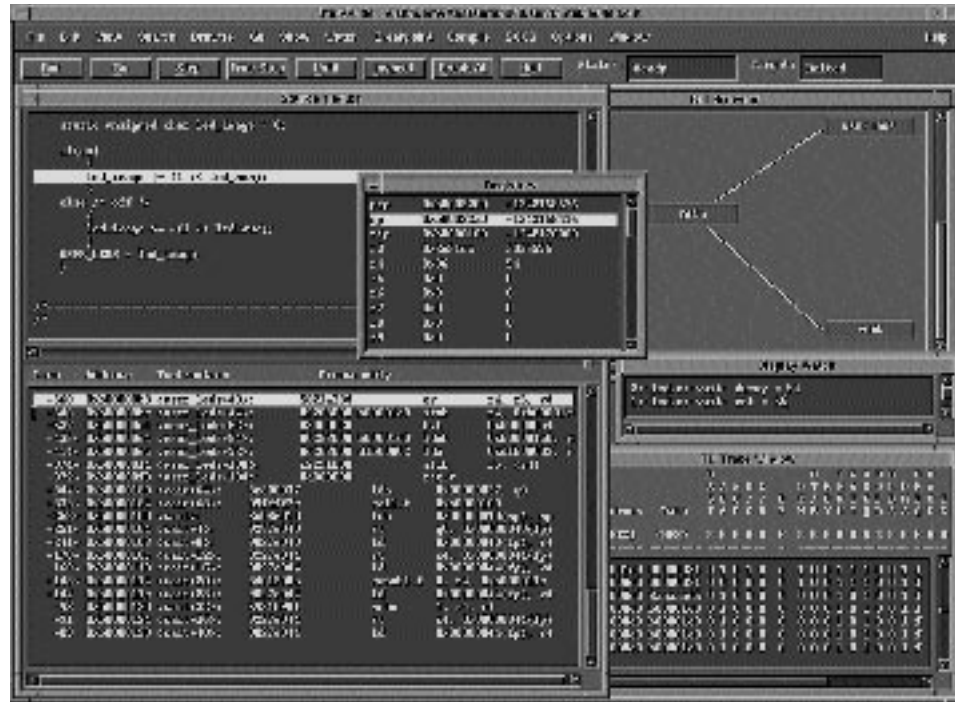


STEPGuide for STEP In-Circuit Express Emulators



- Integrated/Interactive Environment With Transparent Interfaces
- Support for Real-Time Functions
- Real-Time Execution, Debug, Measurement and Feedback
- Complete Reconstruction and Analysis of Cached Instruction Execution
- Non-Intrusive Support of i960® Cx, Jx Processors at Full Speed
- Concurrent Viewing of Compilation Errors, Source, Assembly, Real-Time Execution Information, Hardware Information, Performance and Logic Analysis
- Supports Intel's CTOOLS960, STEP's SGCC960 and Microtec's MCC960 Compilers
- Turnkey Solution for Embedded RISC Application Development Projects
- Also Available as a Software Only Solution with On-Board Target Monitor



STEPGuide is the first development environment for real-time embedded RISC applications in which software and hardware tools are both fully integrated and interactive. This development platform provides a powerful human interface through a mouse-driven, point-and-shoot environment implemented under X Windows/Motif. STEPGuide allows complete access to the features of RISC architectures—full cache reconstruction, nonintrusive execution examination up to the Express emulator's maximum speed (33-80 MHz), and performance and logic analysis.

STEPGuide is designed specifically for embedded RISC microprocessor application support. STEPGuide uniquely combines the latest in program development, project management, software and hardware debug, systems integration and performance analysis tools into a single, efficient integrated package. The unique STEPGuide design translates into higher continuity in application development,

resulting in higher productivity and creativity.

STEPGuide features a comprehensive way to edit, compile, debug and archive embedded control application code. A selection of platforms is available through the control panels for execution of the target code and is available for real-time, in-target testing. Users may elect to use one of STEP's Express RISC in-circuit emulators or use an instruction set simulator or execution vehicle with resident monitor to allow software development to proceed early in the development cycle. For development of ROM code, from .5 to 8 Mbytes of overlay memory is available. Real-time measurement and analysis of code execution is provided by STEPGuide's efficient logic analysis and performance analysis capability and provides actual measured execution statistics of real-time loops. The results are available in easy-to-read, easy-to-use graphical presentations.

HOST SYSTEMS SUPPORTED:
Sun-4, RS/6000, Custom

PROCESSORS SUPPORTED:
i960 Cx, Jx, Hx Processors

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